

Variables	
[define] variable	Define
[define] variable =	Assignment
[define] variable :=	= recursively expanded
[define] variable ::=	:=,::= simply expanded
[define] variable +=	Append
[define] variable ?=	Only if not exists
endif	Multi-line closure
undefine variable	Undefinition
override var-assign	Explicitly overrides all
export	All variables to child process
export <var var-assign>	To child process
Unexport variable	Not export
private var-assign	Don't inherited by prerequisites

Directives	
include file	Inclusion
<-include sinclude> file	
vpath [pattern] [path]	Search paths specification removal
<ifdef ifndef> variable	Value not empty (It doesn't expand)
<ifeq ifneq> (a,b)	Equality
<ifeq ifneq> "a" "b"	
<ifeq ifneq> 'a' 'b'	
else	Else statement
endif	Closure

Functions	
\$(subst f, to, t)	Replace <i>f</i> with <i>to</i> in <i>t</i>
\$(patsubst p, r, t)	Replace words matching <i>p</i> with <i>r</i> in <i>t</i>
\$(t:p=r)	
\$(strip s)	Remove excess spaces from <i>s</i>
\$(findstring s, t)	Locate <i>s</i> in <i>t</i>
\$(filter p..., t)	Words in <i>t</i> that match one <i>p</i> words
\$(filter-out p..., t)	Words in <i>t</i> that <i>don't</i> match <i>p</i> words
\$(sort l)	Sort <i>l</i> lexicographically, removes dup.
\$(word n, t)	Extract the <i>n</i> th word (one-origin) of <i>t</i>
\$(words t)	Count the number of words in <i>t</i>
\$(wordl s, e, t)	List of words in <i>t</i> from <i>s</i> to <i>e</i>
\$(firstword ns...)	Extract the first word of <i>ns</i>
\$(lastword ns...)	Extract the last word of <i>ns</i>
\$(dir ns...)	Directory part of each file name
\$(notdir ns...)	Non-directory part of each file name
\$(suffix ns...)	Deletes form the last '.' in every <i>ns</i>
\$(basename ns...)	Base name (no suffix) in every <i>ns</i>
\$(addsuffix sf, ns...)	Append <i>sf</i> to each word in <i>ns</i>
\$(addprefix pf, ns...)	Prepend <i>pf</i> to each word in <i>ns</i>
\$(join l1, l2)	Join two parallel lists of words
\$(wildcard p...)	Find files matching a pattern (<i>not</i> '%')
\$(realpath ns...)	Absolute name (no ., .., nor symlinks)
\$(abspath ns...)	Absolute name (no. or ..)Preserves symlinks
\$(error t...)	make fatal error with the message <i>t</i>
\$(warning t...)	make warning with the message <i>t</i>
\$(info t...)	make info with the message <i>t</i>

\$(shell c)	Execute a shell cmd, returns output
\$(origin v)	Origin of variable <i>v</i>
\$(flavor v)	Flavor of variable <i>v</i>
\$(foreach v, w, t)	Evaluate <i>t</i> with <i>v</i> bound to each word in <i>w</i> , and concatenate the results
\$(if c, then-part[, else-part])	Evaluates <i>c</i> ; if it's non-empty substitute by <i>then-part</i> otherwise by <i>else-part</i>
\$(or c1[, c2[, c3...])	Evaluate each <i>c<i>N</i></i> ; substitute the first non-empty expansion.
\$(and c1[, c2[, c3...])	Evaluate each <i>c<i>N</i></i> ; if any is empty substitution is empty. Expansion of the last <i>condition otherwise</i>
\$(call v, p, ...)	Evaluates <i>v</i> replacing any references to \$(1), \$(2) with the first, second, etc. <i>p</i> values
\$(eval t)	Evaluate <i>t</i> and read the results as makefile commands
\$(file op f, t)	Open the file <i>f</i> using mode <i>op</i> and write <i>t</i> to that file
\$(value v)	Evaluates <i>v</i> , with no expansion

Functions			
\$\$	\$(@D)	\$(@F)	Name of the target
%%	\$(%D)	\$(%F)	Target member name, when target is an archive member
\$\$<	\$(<D)	\$(<F)	First prerequisite
\$\$?	\$(?D)	\$(?F)	Prerequisites newer than the target
\$\$^	\$(^D)	\$(^F)	Names of all the prerequisites, with spaces between them. \$\$^ omits duplicates
\$\$+	\$(+D)	\$(+F)	
\$\$*	\$(*D)	\$(*F)	implicit rule stem

Special targets (T: As target, P: As prerequisite)	
.PHONY	T: make runs it's prereq. unconditionally
.SUFFIXES	T: list of suffixes used in checking for suffix rules
.DEFAULT	T: Used for any target for which no rules are found
.PRECIOUS	P: Target and intermediate files are not deleted
.INTERMEDIATE	P: treated as intermediate files
.SECONDARY	P: target is intermediate but not deleted
.SECONDEXPANSION	T: prerequisites expand twice
.DELETE_ON_ERROR	T: make will delete the target of a rule if it has changed and its recipe exits with a nonzero exit status
.IGNORE	P: Ignore errors T: Ignore all errors
.LOW_RESOLUTION_TIME	P: Considered as generates low resolution time stamps
.SILENT	P: Muted T: Mute all
.EXPORT_ALL_VARIABLES	T: as export
.NOTPARALLEL	T: make will run serially
.ONESHELL	As target: All lines of a recipe runs on one shell

. POSIX	As target: makefile will be parsed and run in POSIX-conforming mode
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Special variables	
MAKEFILE_LIST	List of parsed makefiles
.DEFAULT_GOAL	Defines the default target (instead the first)
MAKE_RESTARTS	Number of restarts
MAKE_TERMOUT	stdout terminal
MAKE_TERMERR	stderr terminal
.RECIPEPREFIX	Recipe prefix instead of tab
.VARIABLES	List of global variable names (Read only)
.FEATURES	List of features (Read only)
.INCLUDE_DIRS	Makefiles directories inclusion
MAKEFILES	Makefiles to be read
VPATH	Directory search path
SHELL	System default command interpreter
MAKESHELL	(MS-DOS only) command interpreter
MAKE	Name with which make was invoked
MAKE_VERSION	Version number of the GNU make program
MAKE_HOST	Host that GNU make was built to run on
MAKELEVEL	Number of levels of recursion
MAKEFLAGS	Flags given to make
GNUMAKEFLAGS	Other flags parsed by make
MAKECMDGOALS	Targets given to make
CURDIR	Absolute pathname of the CWD
SUFFIXES	Suffixes before make
.LIBPATTERNS	Naming of the libraries make searches for

Rules and prerequisites		
<pf>%<sf>: p... oop... recipes...		Implicit rule. Matching of % is called stem.
target: p... oop... recipes...		Explicit rule.
t: p oop... r...		oop: Order only prerequisites will not update the target.
target-pat: var-assign		Target/pattern specific variables